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Original

6/15/03

**In the United States Patent and Trademark Office**

Serial number 09/340,172

Application filed June 25, 1999

Applicant Derek Wong

Application Title Methods for Increasing Instruction-Level Parallelism in Microprocessors and Digital Systems

Examiner/GAU Eric Coleman / 2183

Mailed December 12, 2002

**Petition for Extension of Time (Rules 136 and 17(a)-(d))**

Outstanding Office Action mailed June 12, 2002

Original period for response expired September 12, 2002

Request for extension of 3 months to December 12, 2002

Small Entity Fee enclosed \$460

To:

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, DC 20231

Sir:

In the above application, applicant respectfully petitions that the period for response to the outstanding Office Action indicated above be extended for the additional months indicated above. A response to such Office Action and the above Petition fee (Small Entity) are enclosed herewith.

Very Respectfully,

*Derek Wong*

Applicant Derek Wong  
Telephone 408-927-7940

**Express Mail Label # ET667071892US Date of Deposit December 12, 2002**

I hereby certify that this paper or fee is being deposited with the United States Postal Service using "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to "Assistant Commissioner for Patents, Washington, DC 20231."

Signed

*Derek Wong*

Inventor